$p_0, \ell_0, 0, 0, *, \Lambda$
protocol state, π, res, n, u, α_{in}
s_1
s_7
$p_2, \ell_1, 0, *, *, *(req) \bullet m_1 \bullet *(off)$
$p_1, \ell_1, 0, *, *, *(req)$
$p_4, \ell_1, 1, num_1, *, *(off)$
$p_5, \ell_1, 1, num_1, m_1, *(off) \bullet m_2 \bullet *(req)$
s_2
$p_2, \ell_2, 0, *, *, *(req) \bullet m_1 \bullet *(off)$
$p_1, \ell_2, 0, *, *, *(req)$
$p_4, \ell_2, 1, num_1, *, *(off)$
$p_5, \ell_2, 1, num_1, m_1, *(off) \bullet m_2 \bullet *(req)$
s_3
$p_2, \ell_3, 0, *, *, *(req) \bullet m_1 \bullet *(off)$
$p_1, \ell_3, 0, *, *, *(req)$
$p_4, \ell_3, 1, num_1, *, *(off)$
$p_5, \ell_3, 1, num_1, m_1, *(off) \bullet m_2 \bullet *(req)$
s_4
$p_2, \ell_4, 0, *, *, *(req) \bullet m_1 \bullet *(off)$
$p_1, \ell_4, 0, *, *, *(req)$
$p_4, \ell_4, 1, num_1, *, *(off)$
$p_5, \ell_4, 1, num_1, *, *(off) \bullet m_2 \bullet *(req)$
s_5
$p_2, \ell_5, 0, *, *, *(req) \bullet m_1 \bullet *(off)$
$p_1, \ell_5, 0, *, *, *(req)$
$p_4, \ell_5, 1, num_1, *, *(off)$
$p_5, \ell_5, 1, num_1, *, *(off) \bullet m_2 \bullet *(req)$
s_6
$p_2, \ell_4, 0, *, m_1, *(off)$
$p_2, \ell_6, 0, *, m_1, *(off)$
$p_2, \ell_7, 1, *, m_1, *(off)$
$p_2, \ell_8, 1, num_1, m_1, *(off)$
$p_3, \ell_9, 1, num_1, m_1, *(off)$
$p_5, \ell_5, 1, num_1, m_2, *(req)$
$p_5, \ell_{10}, 1, num_1, m_2, *(req)$
$p_5, \ell_{11}, 0, num_1, m_2, *(req)$
$p_6, \ell_{12}, 0, num_1, m_2, *(req)$